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PATENT

01/28/03
#1/A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert W. Droege

Serial No.: 09/683,376

Filed: December 19, 2001

For: METHODS AND APPARATUS FOR
OPERATING A SYSTEM

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: Art Unit: 3641
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: Examiner: Keith, Jack W.
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AMENDMENT

Commissioner for Patents
Box NON-FEE AMENDMENT
Washington, D.C. 20231

RECEIVED

JAN 21 2003

GROUP 3600

Sir:

In response to the Office Action dated October 29, 2002, Applicant respectfully requests entry and consideration of the following amendment:

IN THE SPECIFICATION:

Please delete paragraph 21 and replace with the following paragraph:

A) Second logic mode 104 includes a second mode arm switch 210, a second mode initiate switch 212, a plurality of logic functions, such as, but not limited to, an OR gate 220, an OR gate 222, an OR gate 224, an AND gate 226, an AND gate 228, and an AND gate 230. Second logic mode 104 also includes a pulse output timer 240, a pulse output timer 242, a pulse output timer 244, a pulse output timer 246, a delay initiation timer 250, and a flip-flop 260.

PLEASE ADD THE FOLLOWING NEW CLAIMS:

20. A method for fabricating a system having a plurality of modes and interlocks between the modes, said method comprising: